

CPUs - CPU 315-2 PN/DP



Overview

- The CPU with a medium program memory and quantity framework
- High processing performance in binary and floating-point arithmetic
- Used as a central controller on production lines with central and distributed I/O
- Integral PROFINET interface
- Combined MPI / PROFIBUS DP-master/slave interface
- Component Based Automation (CBA) on PROFINET
- PROFINet proxy for intelligent devices on PROFIBUS DP in Component Based Automation (CBA)
- PROFINET IO Controller for operating distributed I/O on PROFINET

[Micro Memory Card required for operation of CPU.](#)

Application

The CPU 315-2 PN/DP is a CPU with a medium-sized program memory. It is used in installations which have distributed automation structures in addition to a centralized I/O. It can be used as a PROFINET IO Controller and as a standard PROFIBUS DP master in the SIMATIC S7-300. The 315-2 PN/DP CPU can also be used as distributed intelligence (DP slave).

The CPU 315-2 PN/DP is an ideal platform for simple technology tasks implemented in software, e.g.:

- Controlling motion with Easy Motion Control.
- Solving closed-loop control tasks with STEP 7 blocks or the runtime software Standard PID Control/Modular PID Control

Extended process diagnostics are possible using SIMATIC S7-PDIAG.

The integrated combination possibilities of the CPU allow networked automation solutions to be implemented without the need for additional components.

Design

The CPU 315-2 PN/DP is equipped with the following:

- Microprocessor;
the processor achieves an execution time of approximately 100 ns per binary instruction and 3 µs per floating-point operation.
- 128 KB RAM (corresponds to approx. 43 K statements);
the extensive working memory for execution-related program sections offers sufficient space for user programs. Micro memory cards (8 MB max.) as load memory for the program also allow the project to be stored in the CPU (complete with symbols and comments) which can be used for data archiving and recipe management.
- Flexible expansion;
up to 32 modules (four-tier configuration).
- Combined MPI/DP interface;
the first integrated MPI/DP interface can establish up to 16 simultaneous connections to S7-300/400PGs, PCs and OPs. Among these connections, one is always reserved for PG and another for OP.
A simple network with up to 32 CPUs can be configured with the MPI by means of "global data communications".
This interface can be reconfigured from an MPI to a DP interface. The DP interface can be used as a DP master or as a DP slave.
PROFIBUS DP interface:
the PROFIBUS DP V1 standard is fully supported. This increases the scope of DP V1 standard slaves in terms of diagnostics and parameterization capability.
- Ethernet interface;
the second integrated interface of the CPU 315-2 PN/DP is a PROFINet interface based on Ethernet TCP/IP.
It supports the following protocols:
 - S7 communication for data communication between SIMATIC controllers;
 - PG/OP communication for programming, start-up and diagnostics through STEP 7;
 - PG/OP communication for interfacing to HMI and SCADA;
 - Open TCP/IP communication over PROFINet and SIMATIC NET OPC server for communication with other controllers and I/O devices with a separate CPU

Function

- Password protection;
a password concept also protects the user program from unauthorized access
- Diagnostics buffer;
the last 100 errors and interrupt events are saved in a buffer for diagnostics purposes
- Maintenance-free data backup;
the CPU automatically saves all data in case of power failure, so that it remains available unchanged when the power is turned on again.

Programmable features

STEP 7 can be used to parameterize both S7 configurations and the properties and responses of the CPUs:

- Interface with multipoint capability (MPI);
definition of station addresses.
- Restart/cycle performance characteristics;
definition of maximum cycle time and cycle loading.
- Clock bit memory;
address settings.
- Protection level;
setting of the access privileges for program and data.
- System diagnostics;
setting of handling and scope of diagnostic messages.
- Time interrupts;
setting of periodic occurrence.
- Time-of-day interrupts;
start date, start time and periodicity settings.
- PROFIBUS DP master/slave interface;
free assignment of addresses for distributed I/O.

Display and information functions

- Status and error displays;
LEDs indicate hardware, programming, time or I/O errors or bus errors and RUN/STOP modes, restart, and the like.
- Testing functions;
the PG can be used to display signal states in program execution, modify process tags irrespective of the user program, and read out the contents of stack memories.
- Information functions;
by means of the PG, the user can obtain information about the memory capacity and operating mode of the CPU, the current workload of the working and load memories, current scan cycle times and diagnostic buffer contents in plaintext.

Integrated communications functions

- PG/OP communication
- Global data communication
- S7 standard communication
- S7 communication
- Open communication over TCP/IP
- PROFINet CBA

System functions

The CPU offers a wide range of system functions for diagnosis, parameterization, synchronization, alarm signaling, time measurement, etc.

For further details, see manual.

Technical specifications

6ES7 315-2EG10-0AB0	
Supply voltages	
Rated value	
– 24 V DC	Yes
– Permitted range, lower limit (DC)	20.4 V
– permissible range, upper limit (DC)	28.8 V
Current consumption	
• Inrush current, typ.	2.5 A
• I ² t	1 A ² s
• Current consumption (idling), type	100 mA
• Power dissipation, typical	3.5 W
Memory	
• Working memory	
– integral	128 KByte
– expandable	No
• Load memory	
– pluggable (MMC)	Yes
– pluggable (MMC), max.	8 MByte
Backup	
– available	Yes
CPU/blocks	
DB	
– Number, max.	1,023
– Size, max.	16 KByte
FB	
– Number, max.	2,048
– Size, max.	16 KByte
FC	
– Number, max.	2,048
– Size, max.	16 KByte
OB	

<ul style="list-style-type: none"> - Size, max. 	16 KByte
Nesting depth	
<ul style="list-style-type: none"> - per priority class - additional levels within an error OB 	8 4
CPU/processing times	
<ul style="list-style-type: none"> ● for bit instruction, min. ● for word instruction, min. ● for integer math, min. ● for floating-point math, min. 	0.1 μ s 0.2 μ s 2 μ s 3 μ s
Timers/counters and their retentive characteristics	
S7 counter	
<ul style="list-style-type: none"> - Number 	256
<ul style="list-style-type: none"> ● of which retentive without battery - adjustable 	Yes
<ul style="list-style-type: none"> ● Retentivity - adjustable 	Yes
<ul style="list-style-type: none"> ● Counting range - adjustable - lower limit - upper limit 	Yes 0 999
IEC counter	
<ul style="list-style-type: none"> - available 	Yes
S7 times	
<ul style="list-style-type: none"> - Number 	256
<ul style="list-style-type: none"> ● Retentivity - adjustable 	Yes
<ul style="list-style-type: none"> ● Timing range - lower limit - upper limit 	10 ms 9,990 s
IEC timer	
<ul style="list-style-type: none"> - available 	Yes
Data areas and their retentive characteristics	
Flags	
<ul style="list-style-type: none"> - Number - Retentivity - Number of clock memories 	2,048 Byte Yes 8
Data blocks	
<ul style="list-style-type: none"> - Number, max. - Size, max. 	1,023 16 KByte
Local data	
<ul style="list-style-type: none"> - per priority class, max. 	1,024 Byte
Address area	
I/O address area	
<ul style="list-style-type: none"> - Inputs - Outputs 	2,048 Byte 2,048 Byte
<ul style="list-style-type: none"> ● of which distributed - Inputs - Outputs 	2 KByte 2 KByte
Process image	
<ul style="list-style-type: none"> - Inputs - Outputs 	128 Byte 128 Byte
Digital channels	
<ul style="list-style-type: none"> - Inputs - Outputs - Inputs, of which central - Outputs, of which central 	16,384 16,384 1,024 1,024
Analog channels	
<ul style="list-style-type: none"> - Inputs - Outputs - Inputs, of which central - Outputs, of which central 	1,024 1,024 256 256
Configuration	

● Racks, max.	4
● Modules per rack, max.	8
Number of DP masters	
- integral	1
- via CP	4
Number of FMs and CPs that can be operated (recommendation)	
- FM	8
- CP, point-to-point	8
- CP, LAN	10
Time	
- Hardware clock (realtime clock)	Yes
- Battery backed and synchronized	Yes
- Deviation per day, max	10 s
Run-time meter	
- Quantity	1
- retentive	Yes
Time synchronization	
- supported	Yes
- on MPI, master	Yes
- on MPI, slave	Yes
- in AS, master	Yes
- in AS, slave	Yes
S7 message functions	
● Number of stations that can log on for message functions, max.	16
● Process diagnostic messages	Yes
● simultaneously active Alarm-S blocks, max.	40
Test and startup functions	
Status/modify	
- Variable	Yes
- Number of variables, max.	30
- of which status variables, max.	30
- of which modify variables, max.	14
Forcing	
- Forcing	Yes
- Forcing, number of variables, max.	10
● Status block	Yes
● Single step	Yes
● Number of breakpoints	2
Diagnostic buffer	
- available	Yes
- Number of inputs, max.	100
- adjustable	No
Communication functions	
● PG/OP communication	Yes
● Routing	Yes
Global data communication	
- supported	Yes
- Size of GD packets, max.	22 Byte
S7 basic communication	
- supported	Yes
S7 communication	
- supported	Yes
S5 compatible communication	
- supported	Yes
Open IE Communication	
- TCP/IP	Yes
- Number of connections, max.	8
- Data length, max.	1,460 Byte
Number of connections	
- overall	16
- usable for PG communication	15
- usable for OP communication	15

<ul style="list-style-type: none"> - usable for S7 basic communication 	14
1st interface	
<ul style="list-style-type: none"> ● Isolated 	Yes
<ul style="list-style-type: none"> ● Power supply to interface (15 to 30 V DC), max. 	200 mA
Functionality	
<ul style="list-style-type: none"> - MPI 	Yes
<ul style="list-style-type: none"> - DP master 	Yes
<ul style="list-style-type: none"> - DP slave 	Yes
<ul style="list-style-type: none"> - Point-to-point connection 	No
MPI	
<ul style="list-style-type: none"> - Number of connections 	16
<ul style="list-style-type: none"> ● Services 	
<ul style="list-style-type: none"> - PG/OP communication 	Yes
<ul style="list-style-type: none"> - Routing 	Yes
<ul style="list-style-type: none"> - Global data communication 	Yes
<ul style="list-style-type: none"> - S7 basic communication 	Yes
<ul style="list-style-type: none"> - S7 communication 	Yes
<ul style="list-style-type: none"> - S7 communication, as client 	Yes
<ul style="list-style-type: none"> - S7 communication, as server 	Yes
<ul style="list-style-type: none"> - Transmission rates, max. 	12 Mbit/s
DP master	
<ul style="list-style-type: none"> ● Services 	
<ul style="list-style-type: none"> - PG/OP communication 	Yes
<ul style="list-style-type: none"> - Routing 	Yes
<ul style="list-style-type: none"> - Global data communication 	No
<ul style="list-style-type: none"> - S7 basic communication 	No
<ul style="list-style-type: none"> - S7 communication 	No
<ul style="list-style-type: none"> - Equidistance support 	Yes
<ul style="list-style-type: none"> - SYNC/FREEZE 	Yes
<ul style="list-style-type: none"> - DPV1 	Yes
<ul style="list-style-type: none"> - Transmission rates, max. 	12 Mbit/s
<ul style="list-style-type: none"> - Number of DP slaves, max. 	124
DP slave	
<ul style="list-style-type: none"> ● Services 	
<ul style="list-style-type: none"> - Routing 	Yes
<ul style="list-style-type: none"> - Global data communication 	No
<ul style="list-style-type: none"> - S7 Basic communication 	No
<ul style="list-style-type: none"> - S7 communication 	No
<ul style="list-style-type: none"> - Direct data exchange (lateral communication) 	Yes
<ul style="list-style-type: none"> - DPV1 	No
<ul style="list-style-type: none"> - Transmission rates, max. 	12 Mbit/s
<ul style="list-style-type: none"> ● Intermediate memory 	
<ul style="list-style-type: none"> - Inputs 	244 Byte
<ul style="list-style-type: none"> - Outputs 	244 Byte
<ul style="list-style-type: none"> - Address areas, max. 	32
2nd interface	
<ul style="list-style-type: none"> ● Isolated 	Yes
<ul style="list-style-type: none"> ● Power supply to interface (15 to 30 V DC), max. 	0 mA
<ul style="list-style-type: none"> ● Automatic transmission speed detection 	Yes
Functionality	
<ul style="list-style-type: none"> - MPI 	No
<ul style="list-style-type: none"> - DP master 	No
<ul style="list-style-type: none"> - DP slave 	No
<ul style="list-style-type: none"> - Point-to-point connection 	No
<ul style="list-style-type: none"> - PROFINET CBA 	Yes
<ul style="list-style-type: none"> - PROFINET IO-Controller 	Yes
PROFINET CBA	
<ul style="list-style-type: none"> - acyclic transmission 	Yes
<ul style="list-style-type: none"> - cyclic transmission 	Yes
PROFINET IO-Controller	
<ul style="list-style-type: none"> ● Services 	
<ul style="list-style-type: none"> - PG/OP communication 	Yes

<ul style="list-style-type: none"> - Routing - S7 communication - Open IE Communication - Transmission rates, max. - Number connectable IO devices, max. 	<p>Yes</p> <p>Yes</p> <p>Yes</p> <p>100 Mbit/s</p> <p>128</p>
<ul style="list-style-type: none"> ● Address area <ul style="list-style-type: none"> - Inputs, max. - Outputs, max. - User data consistency, max. 	<p>8 KByte</p> <p>8 KByte</p> <p>256 Byte</p>
3rd interface	
CPU/ programming	
Programming language	
<ul style="list-style-type: none"> - STEP 7 - KOP - FUP - AWL - SCL - CFC - GRAPH - HiGraph® 	<p>Yes</p> <p>Yes</p> <p>Yes</p> <p>Yes</p> <p>Yes</p> <p>Yes</p> <p>Yes</p> <p>Yes</p>
Software library	
<ul style="list-style-type: none"> ● Bracket levels ● User program protection/password protection 	<p>8</p> <p>Yes</p>
Dimensions and weight	
<ul style="list-style-type: none"> ● Width ● Height ● Depth ● Weight, approx. 	<p>80 mm</p> <p>125 mm</p> <p>130 mm</p> <p>460 g</p>